



cket No. 740756-2414

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT Application of

Shunpei YAMAZAKI et al.

Serial No. 10/028,276

Filed: December 28, 2001

For: SEMICONDUCTOR DISPLAY DEVICE

CORRECTING SYSTEM AND CORRECTING

METHOD OF SEMICONDUCTOR DISPLAY DEVICE

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) Art Unit: 2673

) Examiner: J. Piziali

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on 2/1/2002.

Reginald R. Chubb

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents and Trademarks
Washington, D.C. 20231

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Technology Center 2000

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference listed on the attached Form PTO-1449 be made of record in the above-identified application.

The "Shimokawa et al." reference was previously listed incorrectly on Form PTO-1449 of the Information Disclosure Statement filed on December 28, 2001. Applicants respectfully request the Examiner to line through the incorrect page numbers and consider the correct page numbers, namely 751-758, as they appear on the enclosed Form PTO-1449.

A copy of the reference is **not** submitted herewith in accordance with 37 C.F.R. 1.97(d) since a copy was provided in parent application Serial No. 09/144,538.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380 (740756-2414).

Respectfully submitted,

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